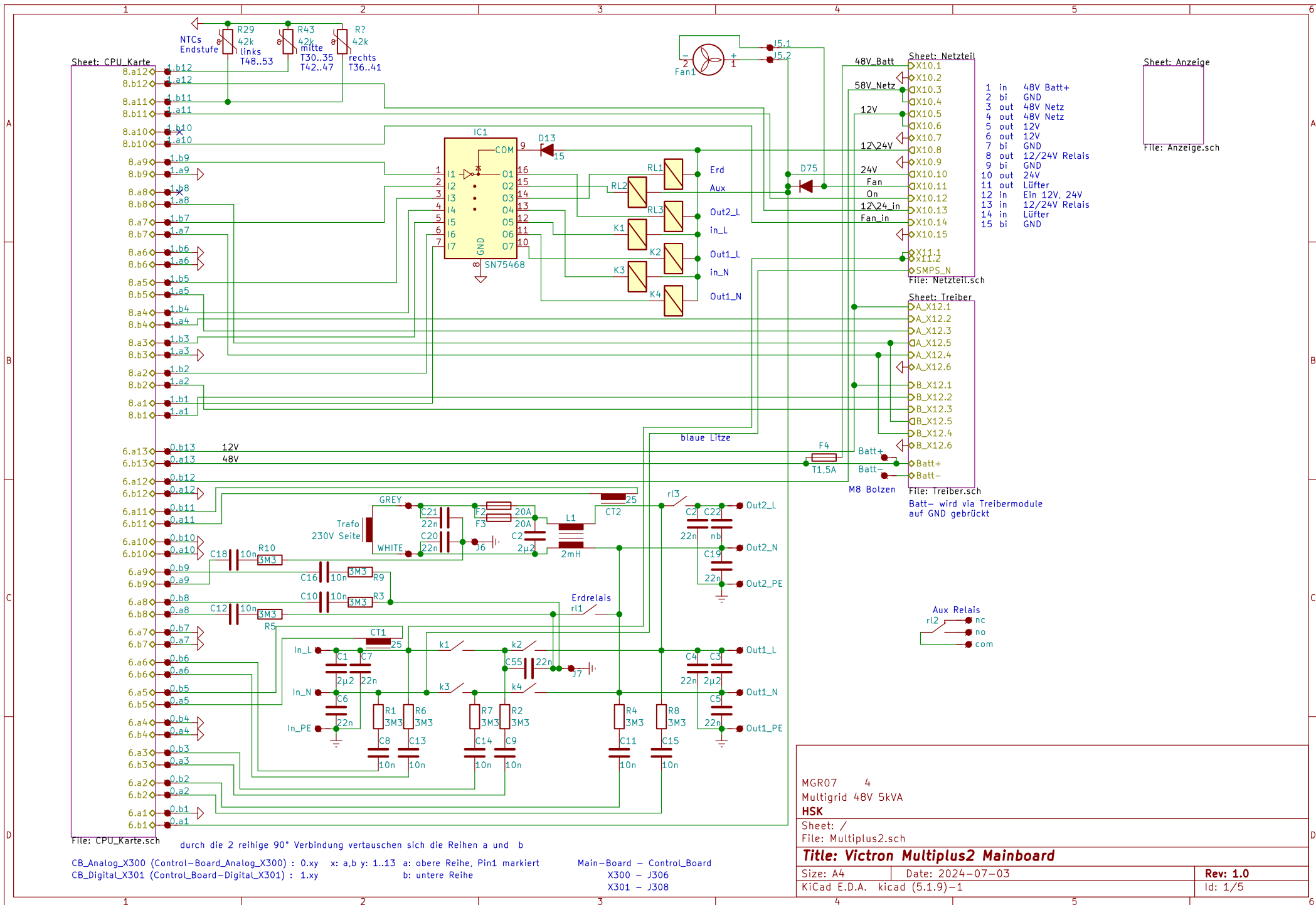




File: CPU_Karte.sch

durch die 2 reihige 90° Verbindung vertauschen sich die Reihen a und b

CB_Analog_X300 (Control-Board-Analog_X300) : 0.xy x: a,b y: 1..13 a: obere Reihe, Pin1 markiert
CB_Digital_X301 (Control-Board-Digital_X301) : 1.xy b: untere Reihe

Main-Board - Control_Board
X300 - J306
X301 - J308

MGR07 4
Multigrig 48V 5kVA
HSK

Sheet: /
File: Multiplus2.sch

Title: Victron Multiplus2 Mainboard

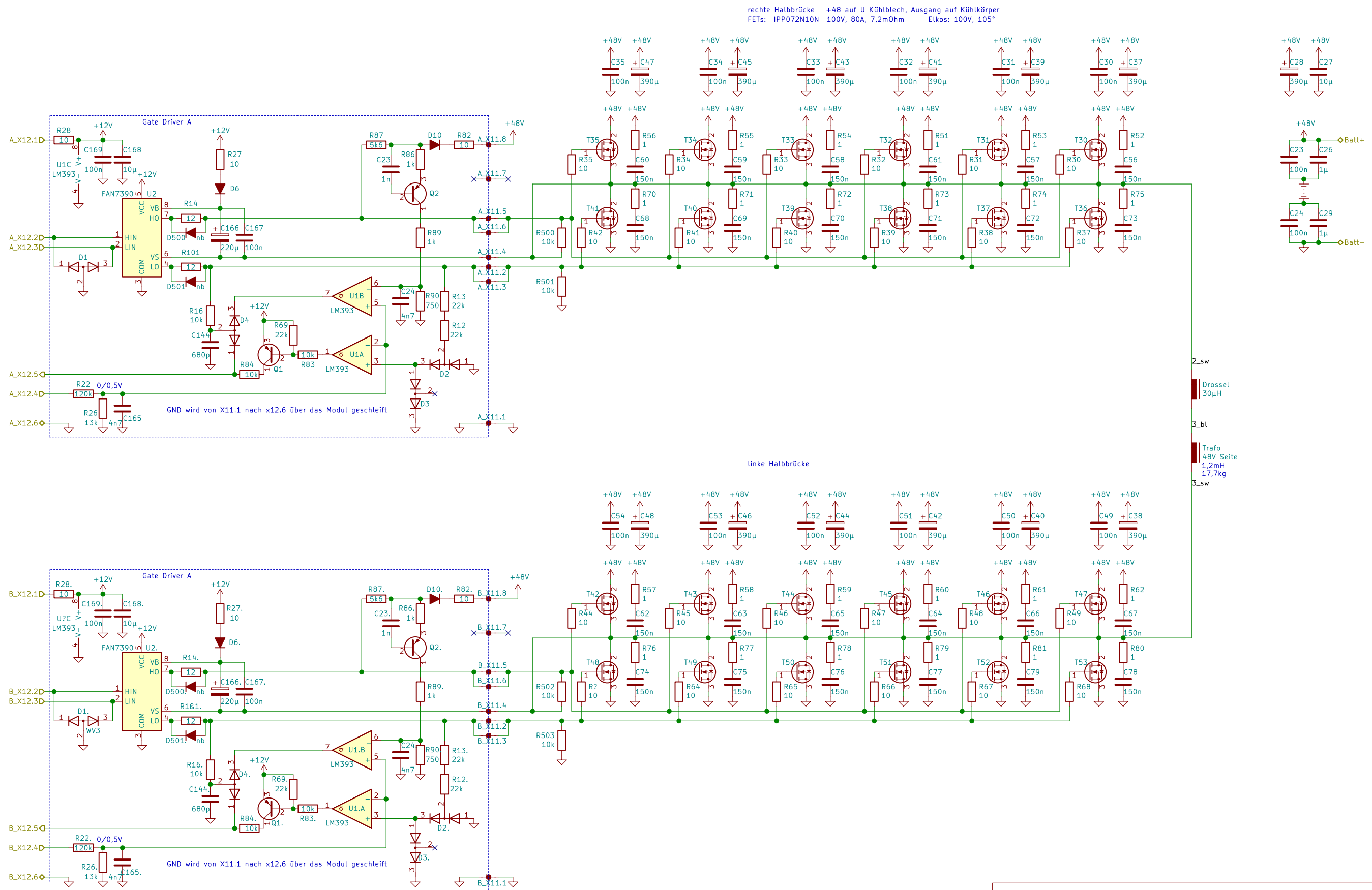
Size: A4 Date: 2024-07-03

KiCad E.D.A. kicad (5.1.9)-1

Rev: 1.0

Id: 1/5

Id: 2/5



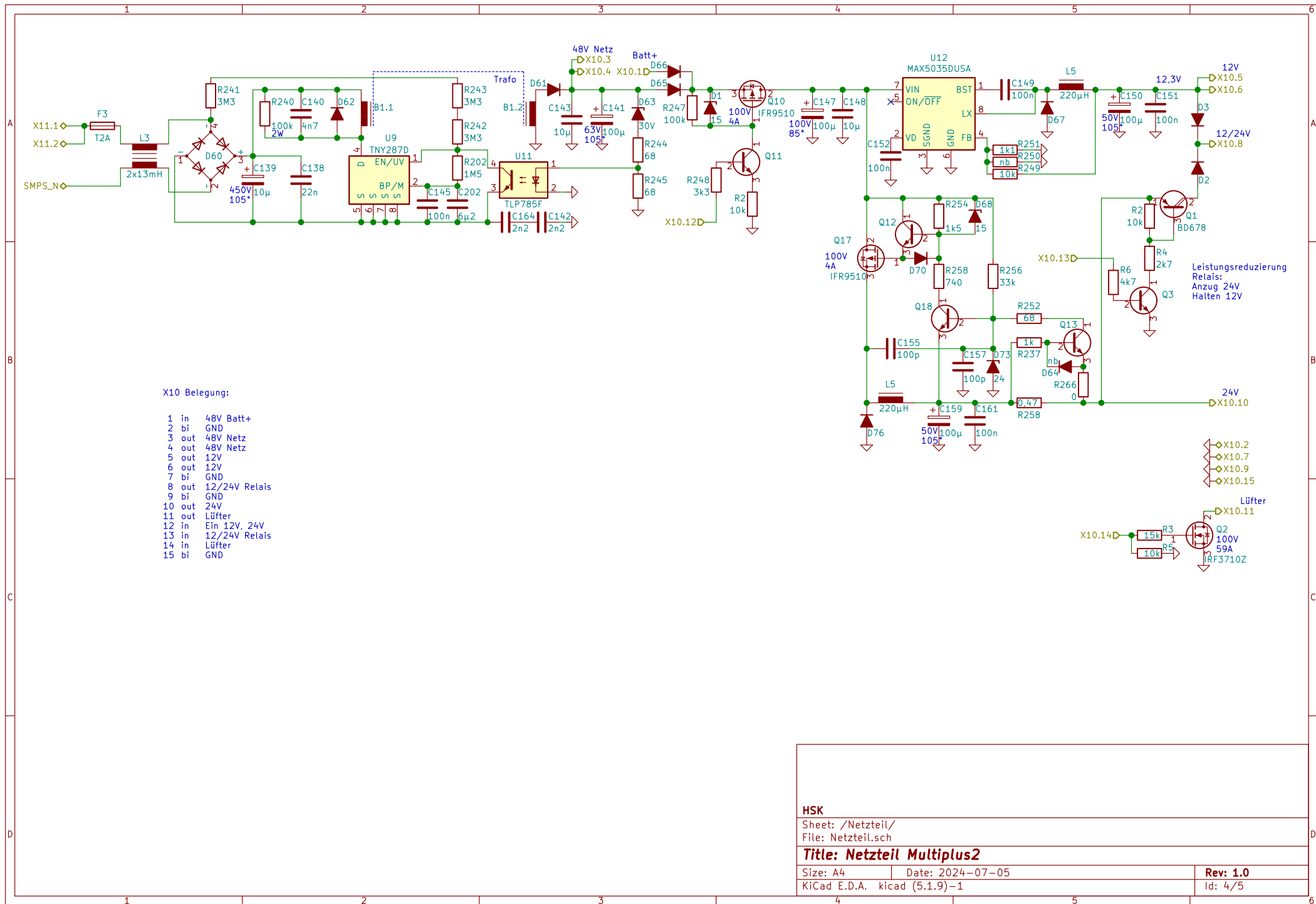
HSK

Sheet: /Treiber/
File: Treiber.sch

Title: Endstufe und Treiber Multiplus2

Size: A3 Date: 2024-07-03
KiCad E.D.A. kicad (5.1.9)-1

Rev: 1.0
Id: 3/5



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