

Pinbelegung CPU und LCD

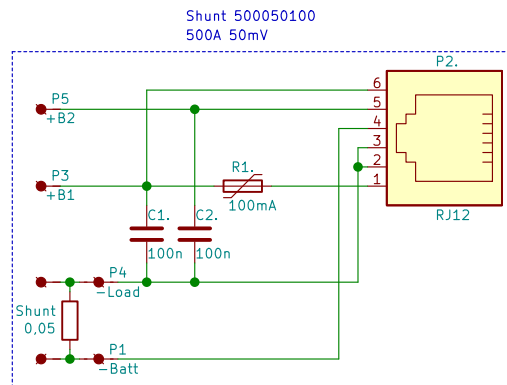
CPU: M430F427

1	Vcc	3V3
2	A0+	Batt-
3	A0-	Load-
4	A1+	Batt+
5	A1-	GND
6	A2+	Batt2+
7	A2-	GND
8	Xin	Xtal
9	Xout	Xtal
10	Vref	C24
11	P2.2	SW1 Left
12	S0	Lcd.3
13	S1	13
14	S2	Lcd.4
15	S3	15
16	S4	Lcd.5

17	S5	Lcd.28
18	S6	Lcd.6
19	S7	Lcd.27
20	S8	Lcd.7
21	S9	Lcd.26
22	S10	Lcd.8
23	S11	Lcd.25
24	S12	Lcd.9
25	S13	Lcd.24
26	S14	Lcd.10
27	S15	Lcd.23
28	S16	Lcd.11
29	S17	Lcd.22
30	S18	Lcd.12
31	S19	Lcd.21
32	S20	Lcd.13

33	S21	Lcd.20
34	S22	Lcd.14
35	S23	Lcd.19
36	Com0	Lcd.1
37	Com1	Lcd.2
38	Com2	Lcd.32
39	Com3	Lcd.31
40	R03	R19
41	R13	R5
42	R23	R11
43	R33	C5
44	S24	Lcd.15
45	S25	Lcd.18
46	S26	Lcd.16
47	S27	Lcd.17
48	P1.5	SW4 Up

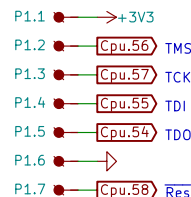
49	P1.4	SW3 Down
50	P1.3	SW2 Right
51	P1.2	LED Backlight
52	P1.1	Relais
53	P1.0	Piezo
54	TD0	54 Debug/
55	TDI	55 Prog
56	TMS	56 "
57	TCK	57 "
58	Res	58 "
59	RxD0	RxD VEdirect
60	TxD0	TxD VEdirect
61	61	61
62	AVss	GND
63	Vss	GND
64	AVcc	3.3V RC



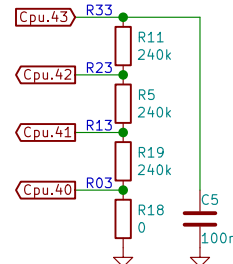
VEdirect



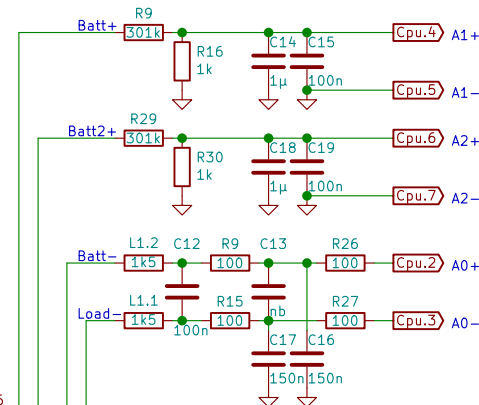
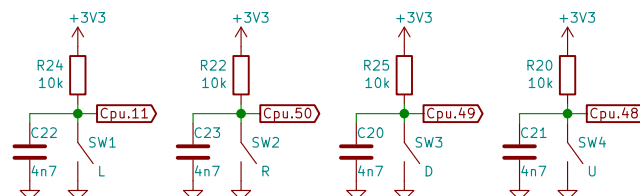
Programmierung



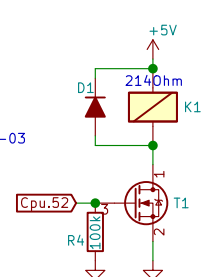
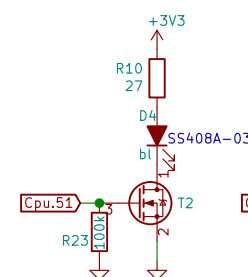
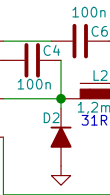
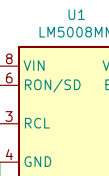
LCD Spannungen



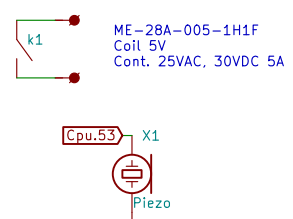
Tasten



CPU enthält 3 Ops ,die als Differenzverstärker konfiguriert werden können.



Schaltausgang



22.05.25 erst.

HSK

Sheet: /

File: BMV600S.sch

Title: BMV600S

Size: A4

Date: 2025-05-25

KiCad E.D.A. kicad (5.1.9)-1

Rev: 1.1

Id: 1/1